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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Naoki MATSUOKA, et al.
Serial No : 09/942,979
Filed : August 30, 2001
Title : PACKET SWITCH

June 10, 2002

Attn: Customer Corrections Division
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REQUEST FOR CORRECTED FILING RECEIPT

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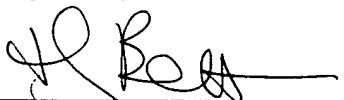
We received the **Filing Receipt** on the above-referenced case, copy enclosed, wherein the **PTO** incorrectly listed the **FILING DATE** as **AUGUST 31, 2001**. Please change the same to read: **AUGUST 30, 2001**. As we cannot locate the original Express Mail Receipt (EL639693967US), we enclose herewith a delivery record from the United States Postal Service, showing that the application was received in the United States Patent and Trademark Office on August 31, 2001 at 6:50 A.M. The only way the application could be received at the USPTO on August 31, 2001 at 6:50 A.M. is if it was deposited with the USPS on August 30, 2001. Thus, August 30, 2001 is the proper filing date.

We also enclose a copy of the transmittal sheet and the first page of the specification, showing the Express Mail Item Number.

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07/26/2002 AKELLEY 00000020 501290 09942979
01 FC:122 130.00 CH

Respectfully submitted,


Thomas J. Bean
Reg. No. 44,528

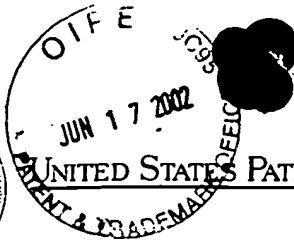
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/942,979	08/31/2001 08/30/2001	2185	870	100794- 11772 (FUJG 18.949	23	12	1

026304
ROSENMAN & COLIN LLP
575 MADISON AVENUE
NEW YORK, NY 10022-2585

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OC000000007145737

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Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-395741 12/26/2000

If Required, Foreign Filing License Granted 10/15/2001

Projected Publication Date: 06/27/2002

Non-Publication Request: No

Early Publication Request: No

Title

Packet switch

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Preliminary Class

711

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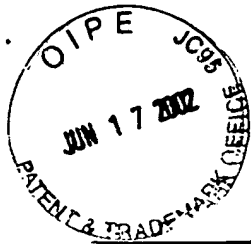
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**UTILITY
PATENT APPLICATION
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(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. **FUJG 18.949**
First Inventor **N. MATSUOKA**
Title **PACKET SWITCH**
Express Mail Label No. **EL639693967US**

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☐ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.
See 37 CFR 1.27.
3. ☒ Specification [Total Pages **41**]
(preferred arrangement set forth below)
- Descriptive title of the invention
- Cross Reference to Related Applications
- Statement Regarding Fed sponsored R & D
- Reference to sequence listing, a table,
or a computer program listing appendix
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings (if filed)
- Detailed Description
- Claim(s)
- Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets **23**]
5. Oath or Declaration [Total Pages **1**]
a. ☐ Newly executed (original or copy)
Copy from a prior application (37 CFR 1.63 (d))
b. ☐ (for continuation/divisional with Box 18 completed)
i. ☐ **DELETION OF INVENTOR(S)**
Signed statement attached deleting inventor(s)
named in the prior application, see 37 CFR
1.63(d)(2) and 1.33(b).
6. ☐ Application Data Sheet. See 37 CFR 1.76

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7. ☐ CD-ROM or CD-R in duplicate, large table or
Computer Program (Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
a. ☐ Computer Readable Form (CRF)
b. Specification Sequence Listing on:
i. ☐ CD-ROM or CD-R (2 copies); or
ii. ☐ paper
c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement ☐ Power of
(when there is an assignee) Attorney
11. ☐ English Translation Document (if applicable)
12. ☒ Information Disclosure ☒ Copies of IDS
Statement (IDS)/PTO-1449 Citations
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
15. ☒ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
16. ☐ Request and Certification under 35 U.S.C. 122
(b)(2)(B)(i). Applicant must attach form PTO/SB/35
or its equivalent.
17. ☐ Other: _____

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP)

of prior application No.: _____

Prior application information:

Examiner: _____

Group Art Unit: _____

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

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Title of the Invention

PACKET SWITCH

BACKGROUND OF THE INVENTION

The present invention relates to a packet switch for outputting packets input through an input line after allocating them to a plurality of output lines.

With an increasing number of Internet users at home and with the remarkable growth of Internet business, an Internet backbone network of a larger capacity and higher quality is strongly demanded. Currently, Internet is mainly operated in best-effort communications without quality guarantee. However, Internet in the future generation is expected to provide realtime data communications using voice, video data, etc. in addition to the best-effort communications. Therefore, it is necessary for a network node to provide the service quality QoS (Quality of Service) appropriate for various communications services by acquiring a terabit switching capacity with which various types of traffic can be flexibly accommodated. It is considered that the probable means for realizing a node having a terabit switching capacity is an input buffer packet switch capable of performing a process at a high memory access speed.

Various scheduling algorithms have been conventionally suggested for input buffer packet switches. They can be roughly grouped into the following two methods based on the assignment of scheduling functions, that is, a method of distributing and assigning the scheduling functions to interface cards, etc. and a method of assigning the scheduling functions collectively to exclusive cards, etc.

FIGS. 22 and 23 show the distribution of the scheduling functions in the conventional input buffer packet switch. FIG. 22 shows the configuration of the packet switch in which the